

FIG. 1

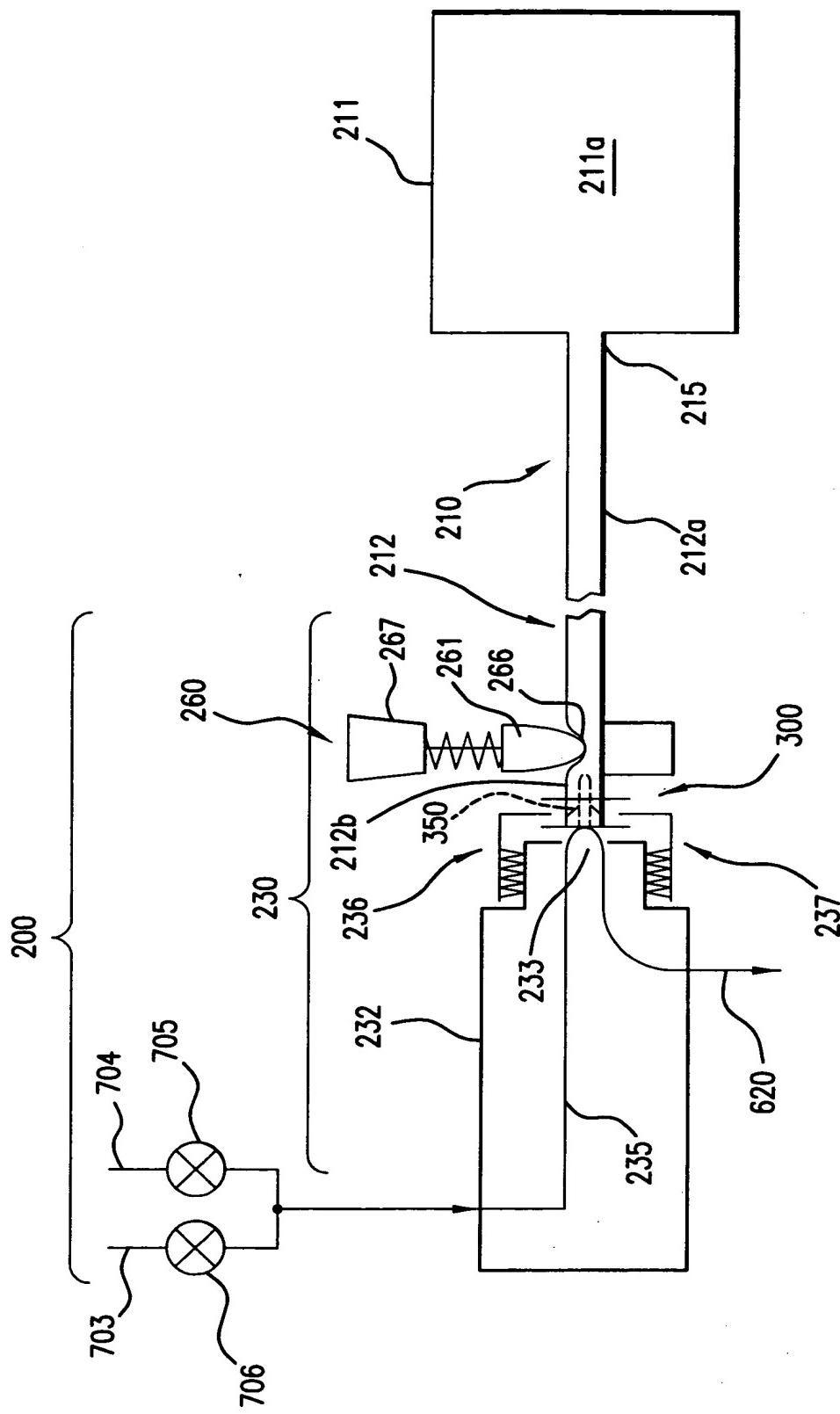


FIG. 2

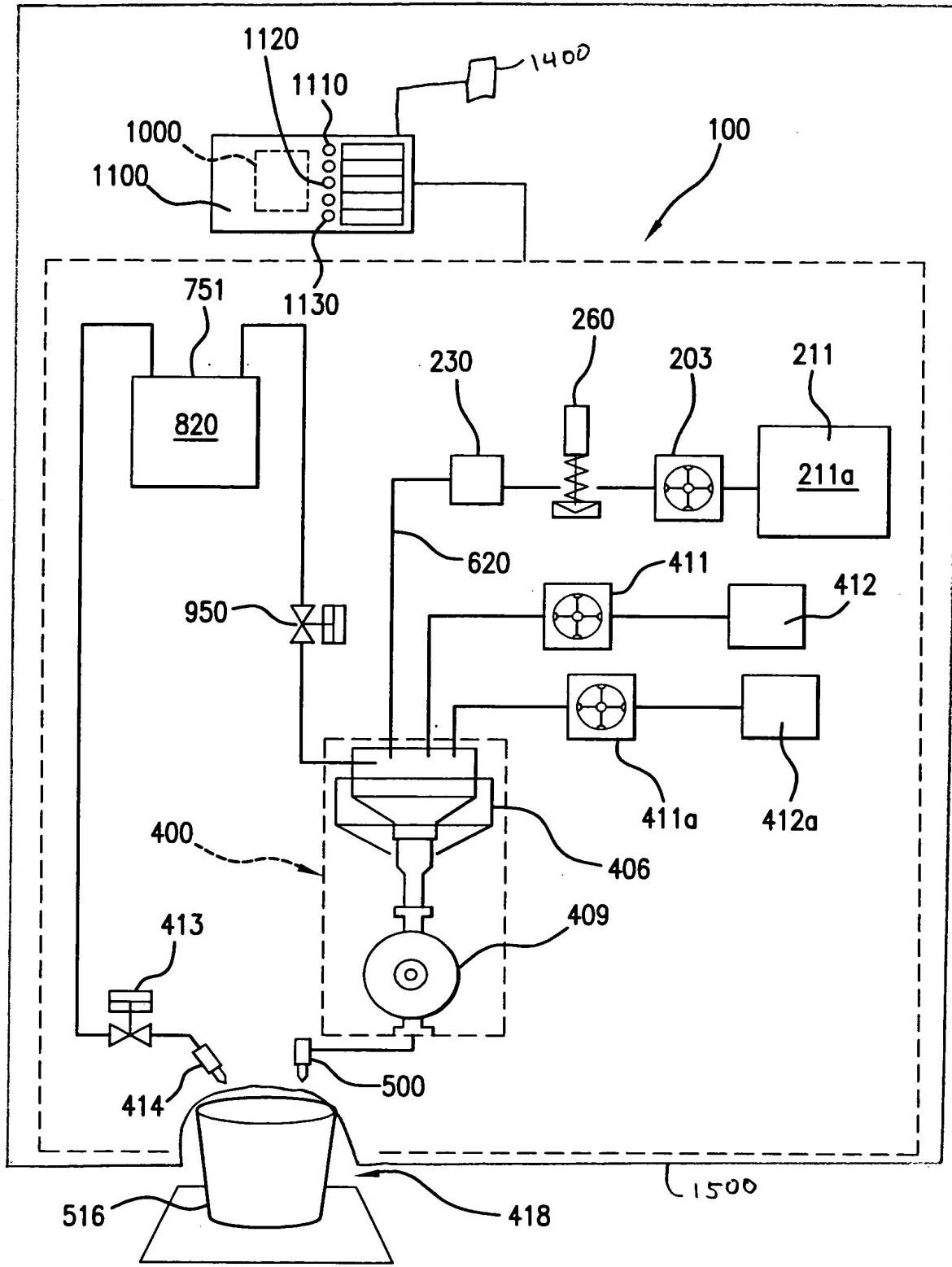
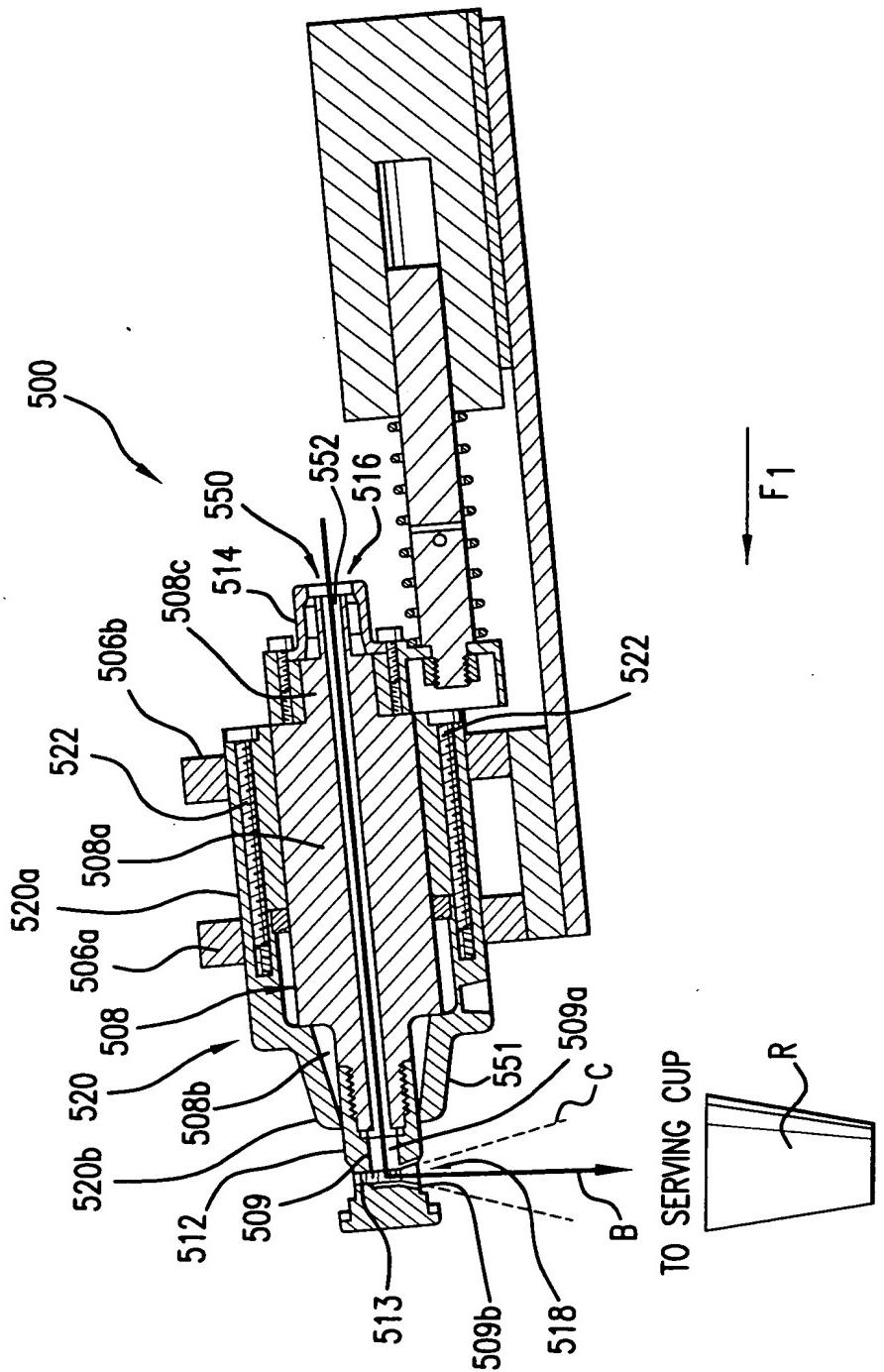
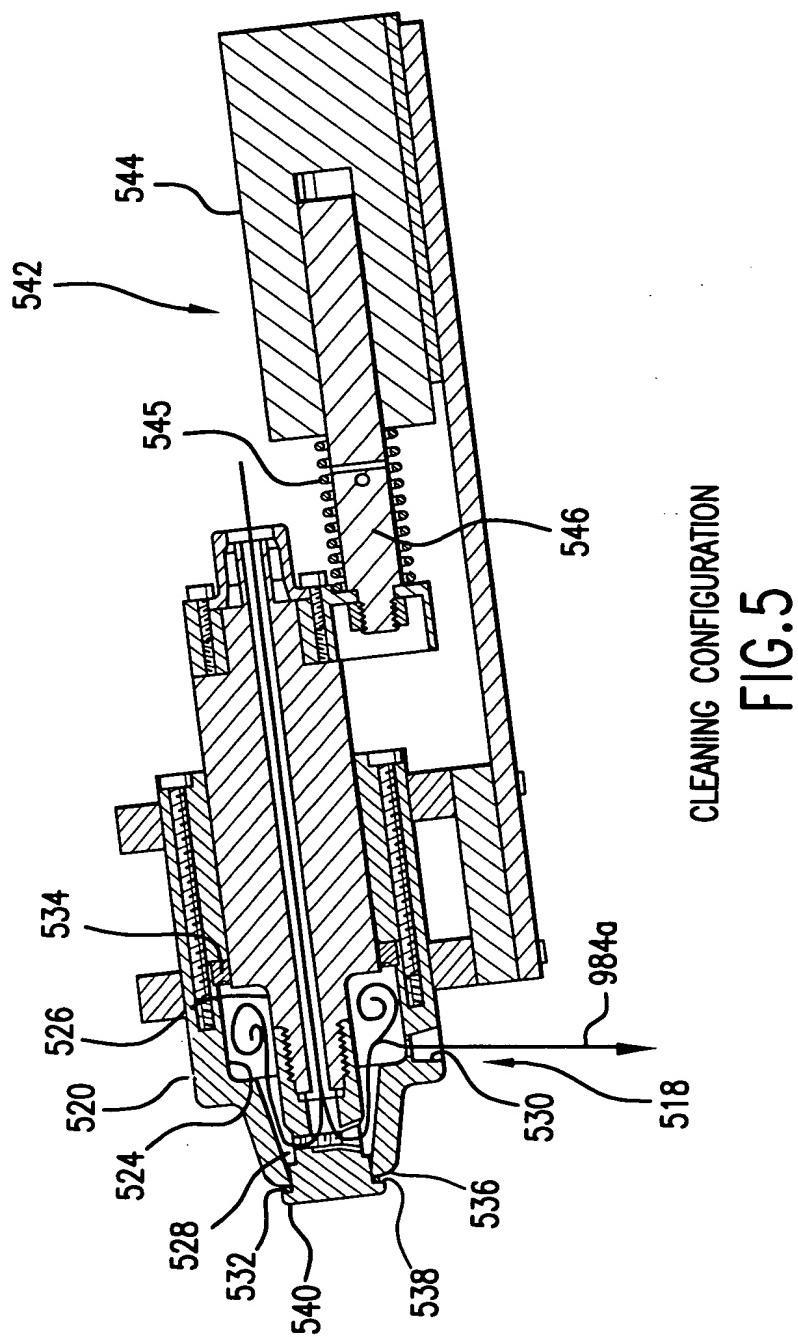


FIG.3



DISPENSE CONFIGURATION
FIG. 4



CLEANING CONFIGURATION
FIG. 5

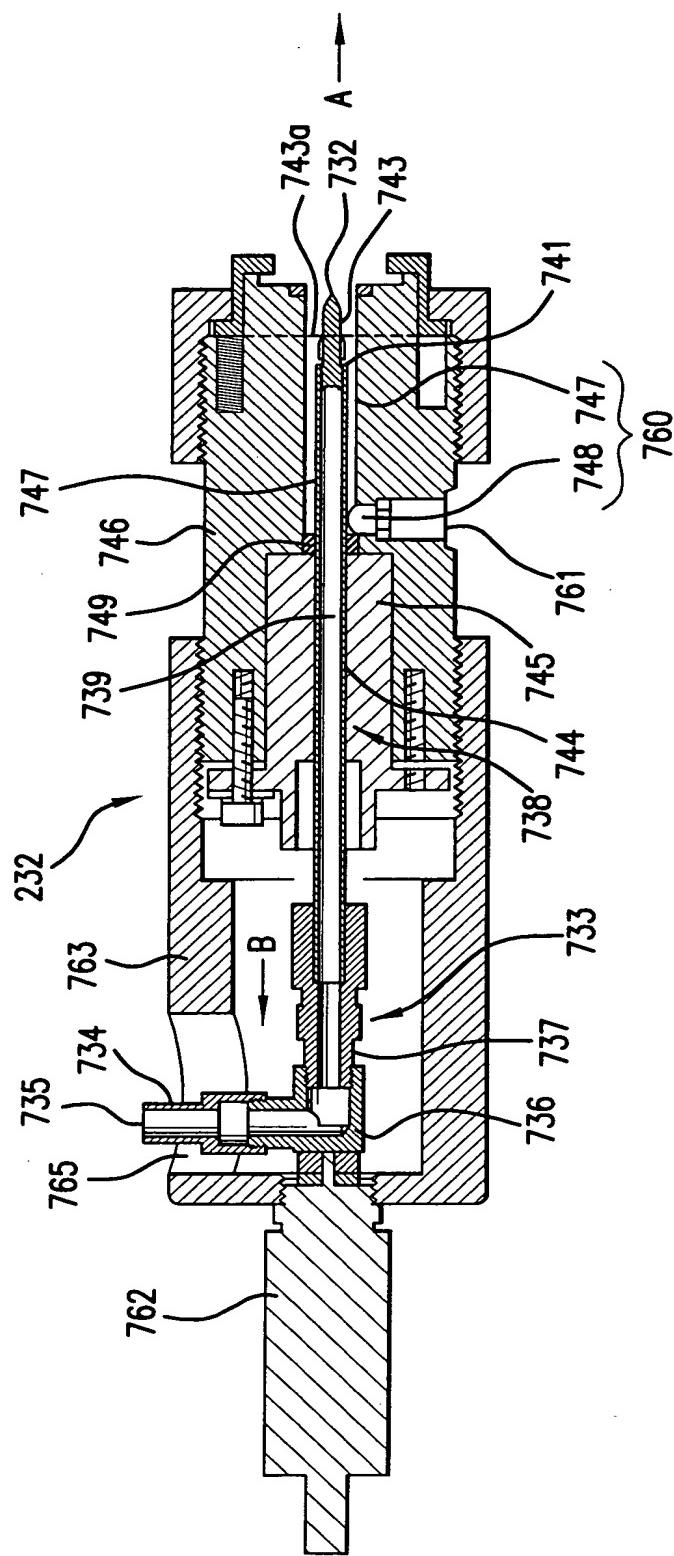
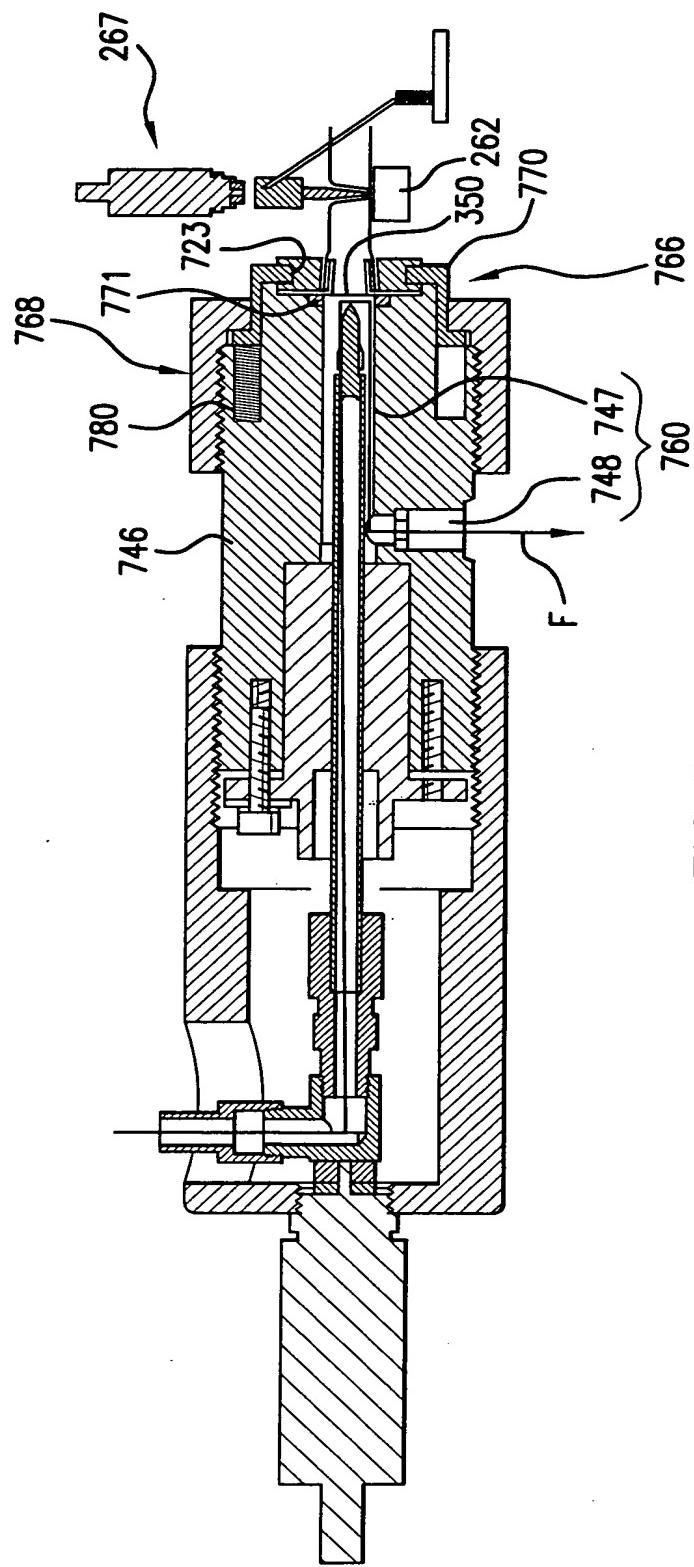
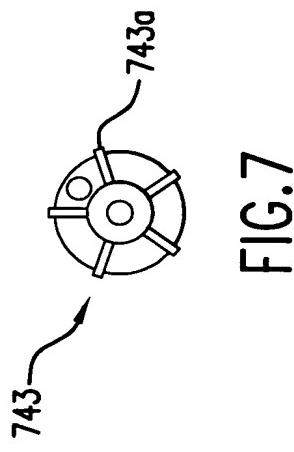


FIG. 6



8120

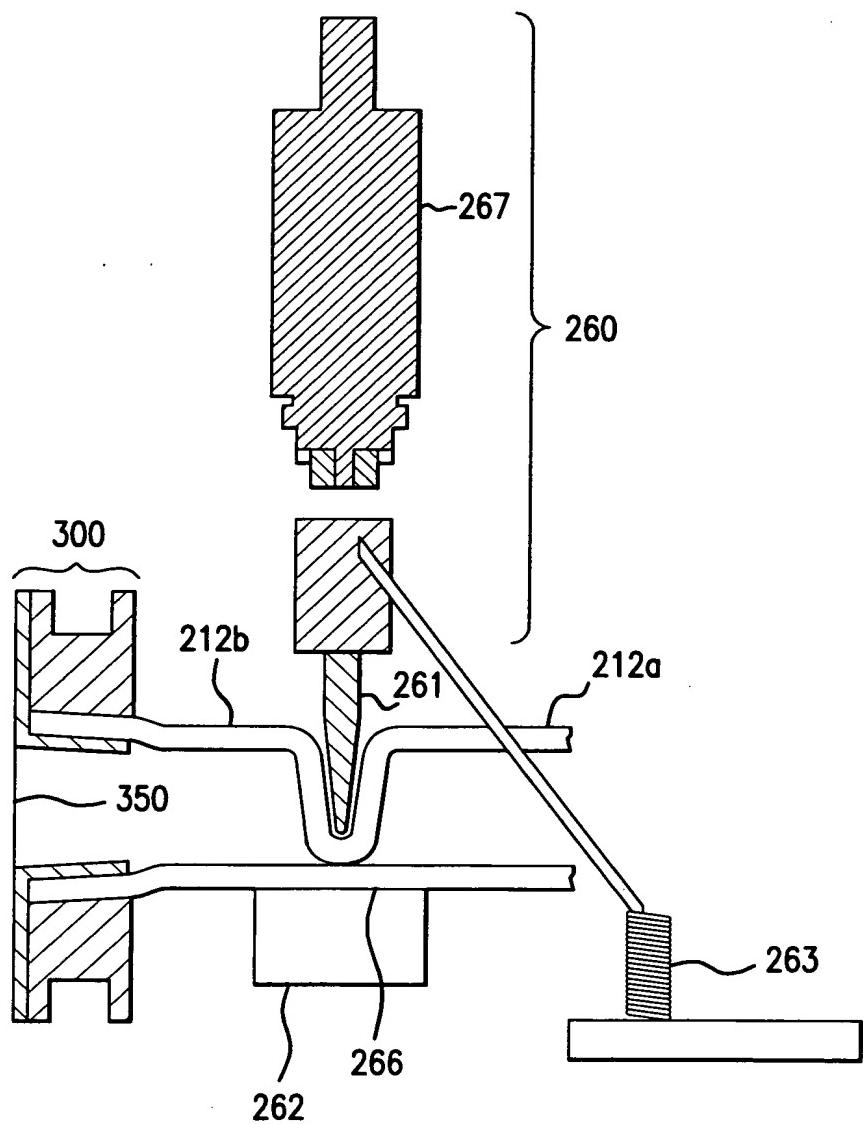


FIG.9

9120

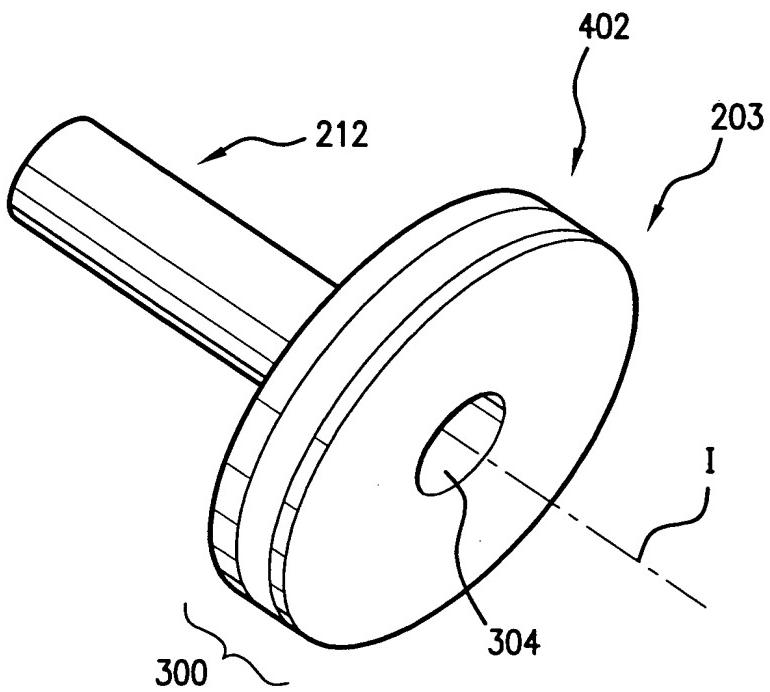


FIG.10

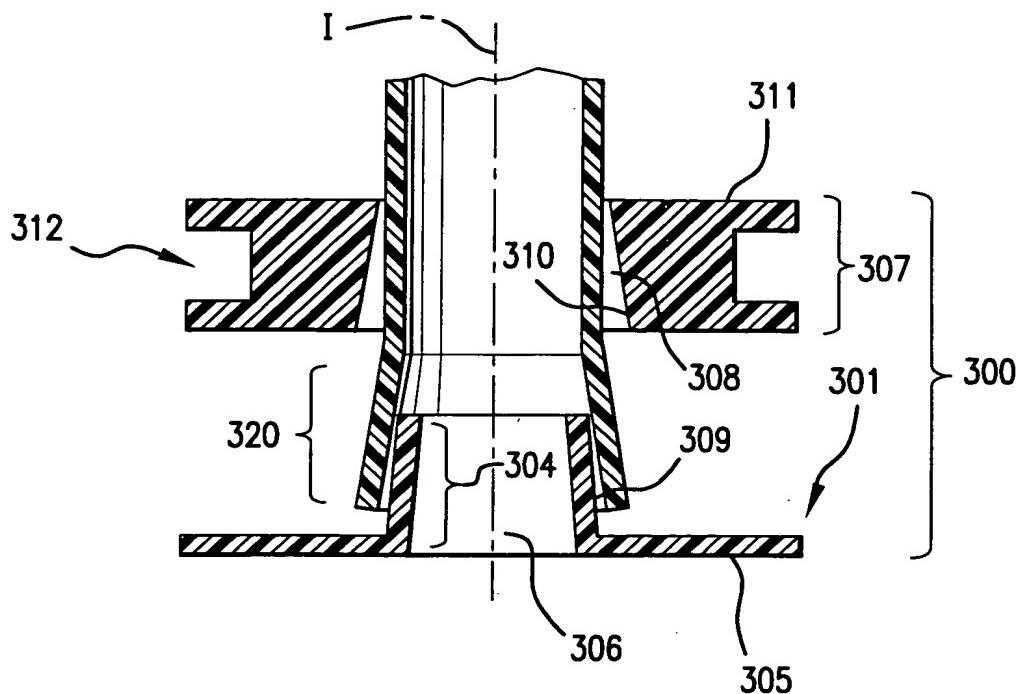


FIG. 11

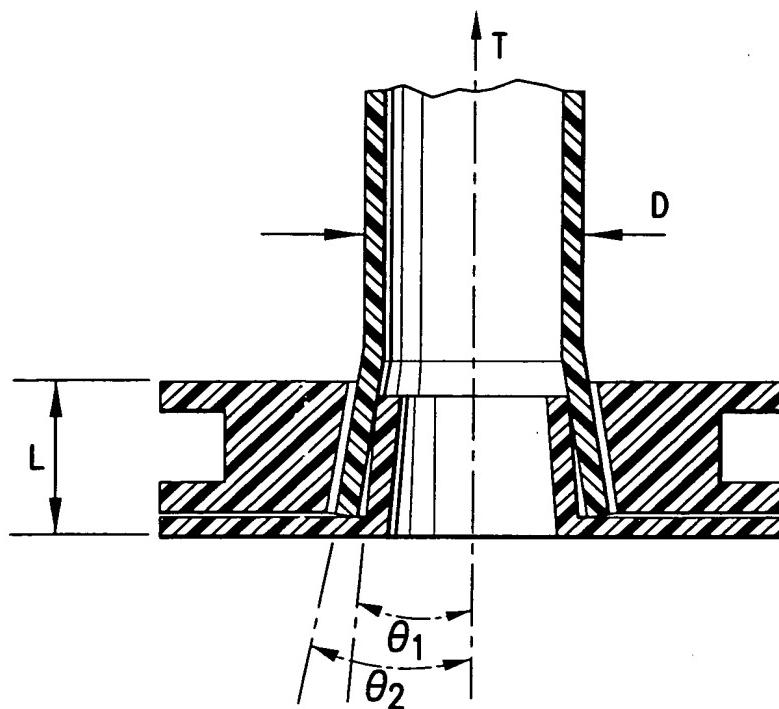


FIG. 12

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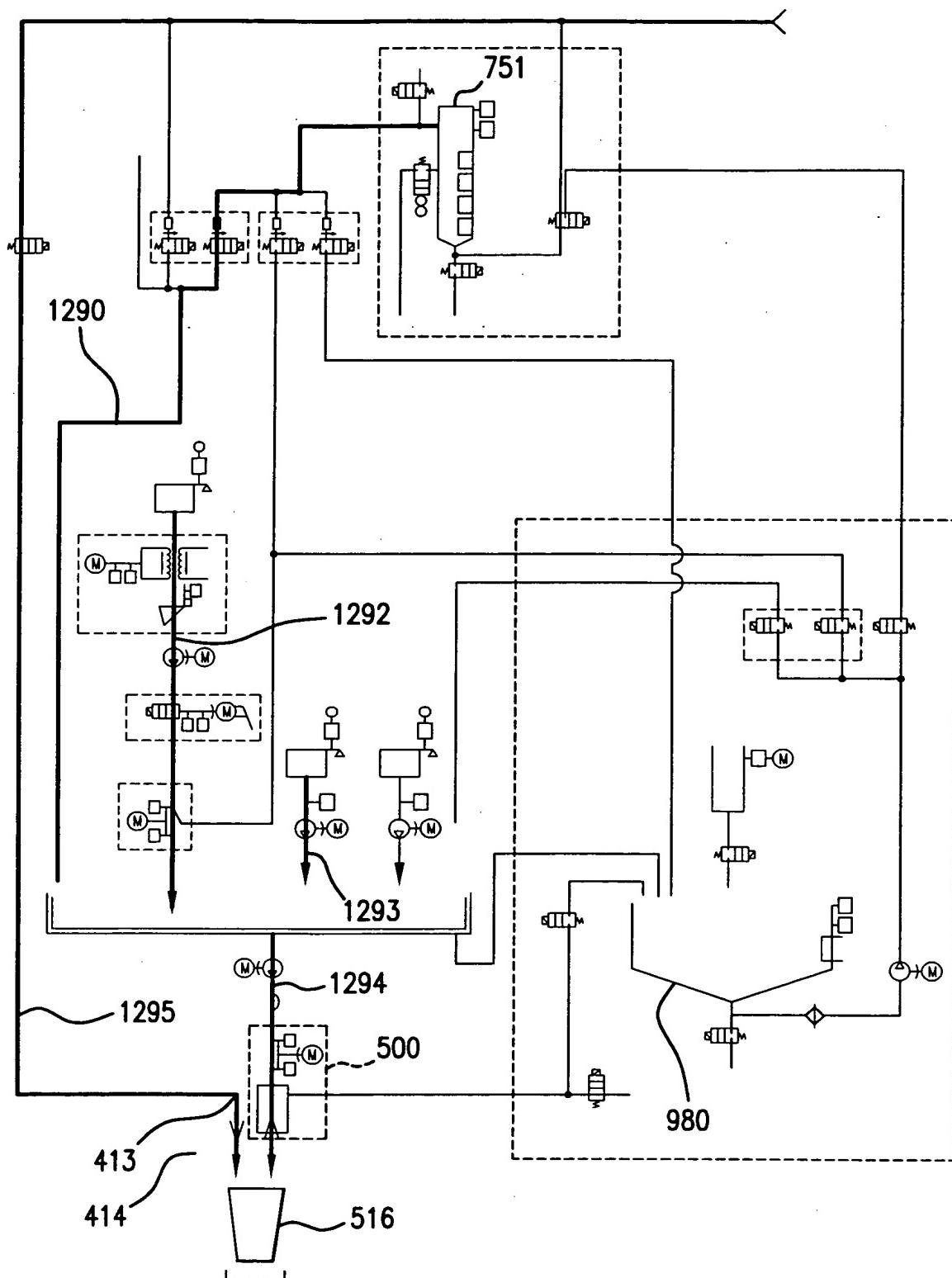


FIG.13

12120

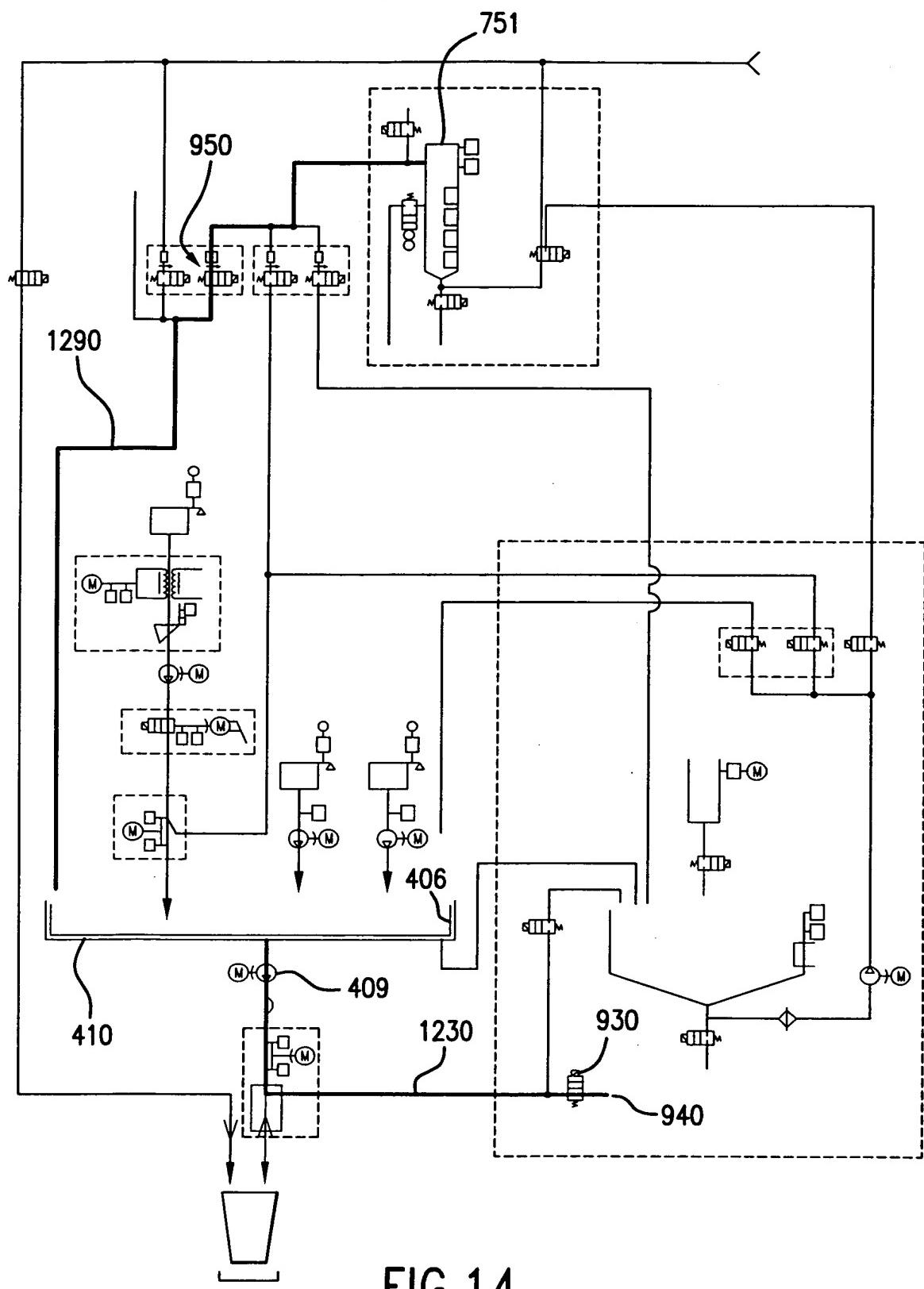


FIG.14

13120

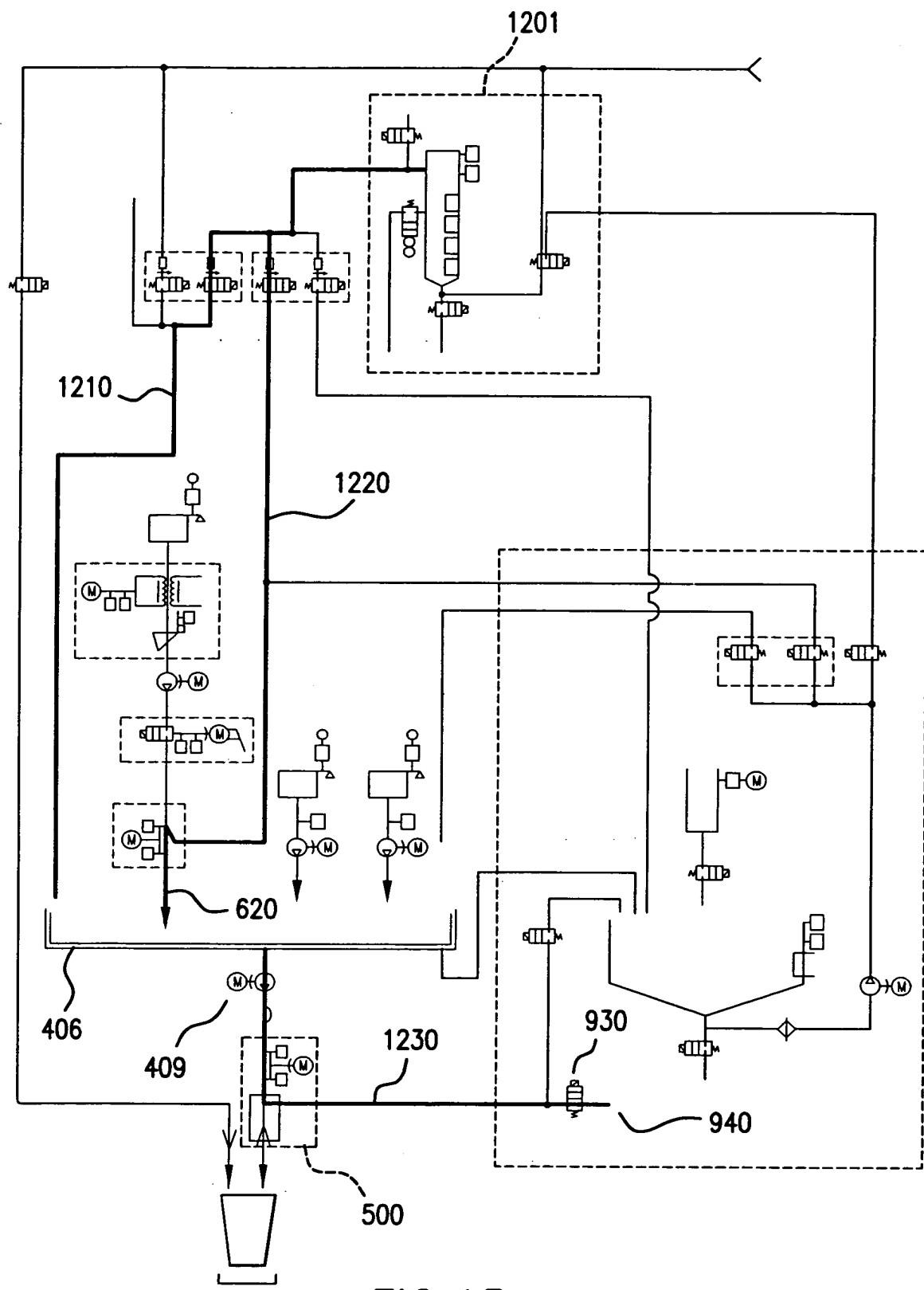


FIG.15

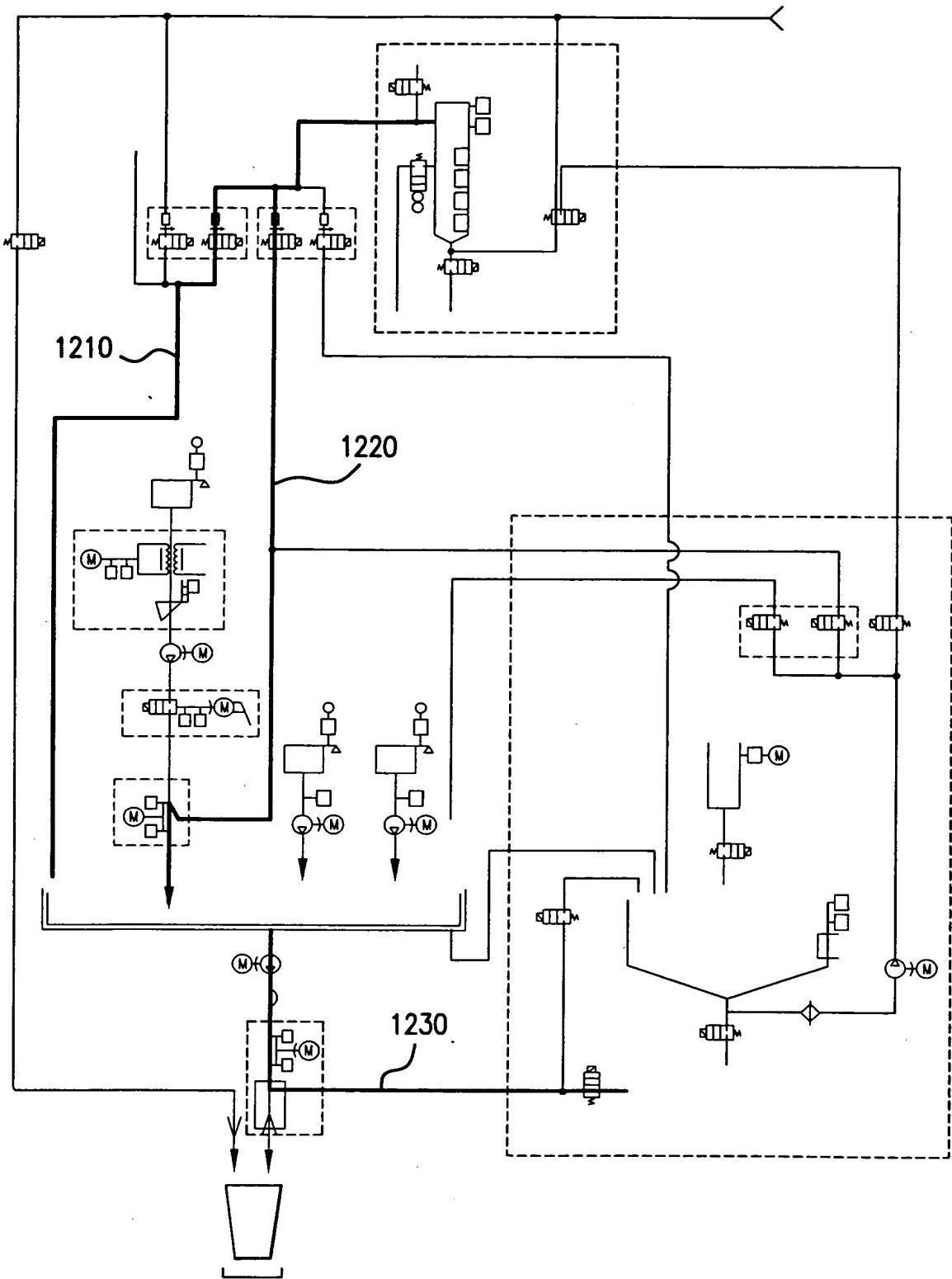


FIG.16

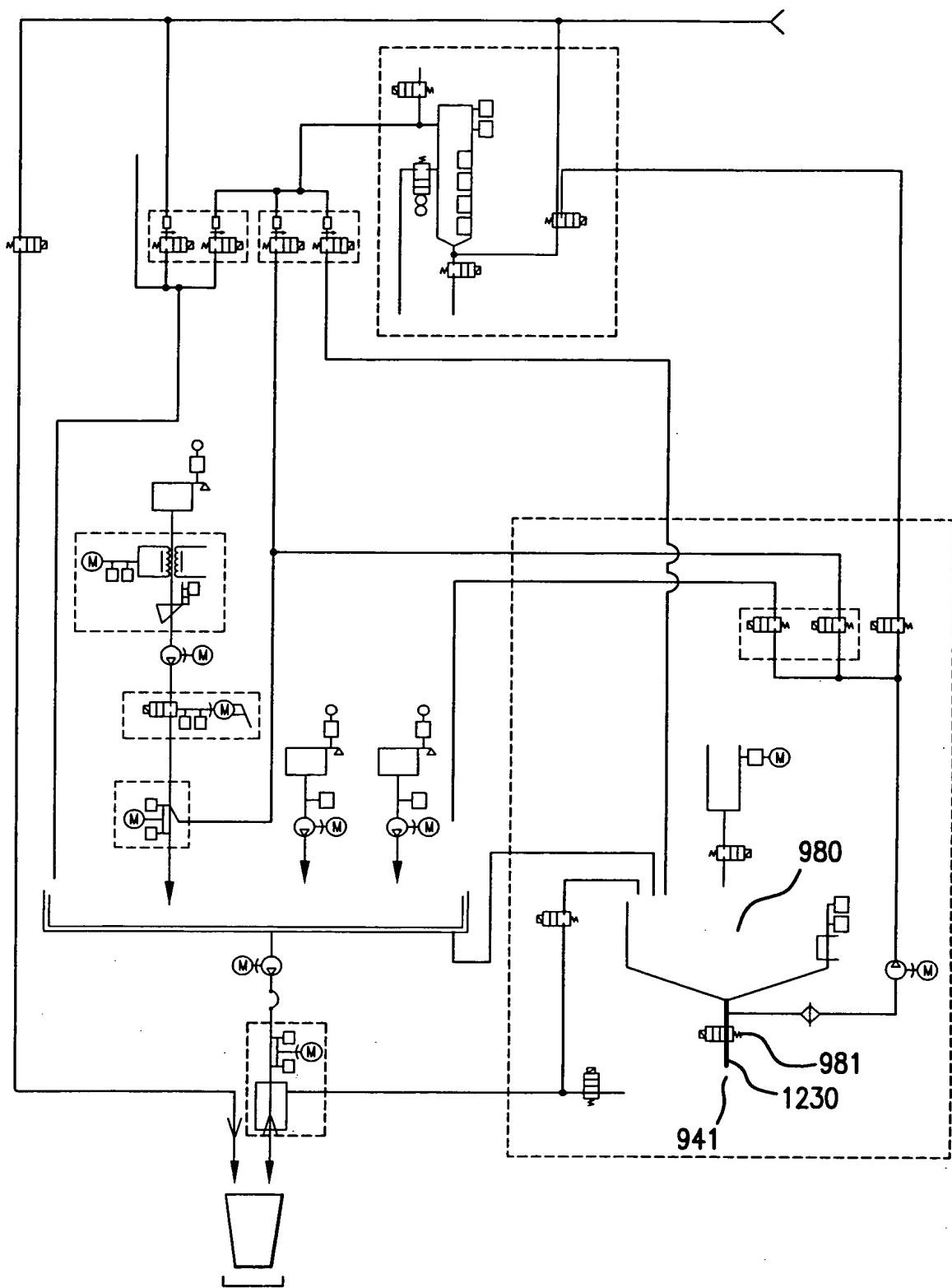


FIG.17

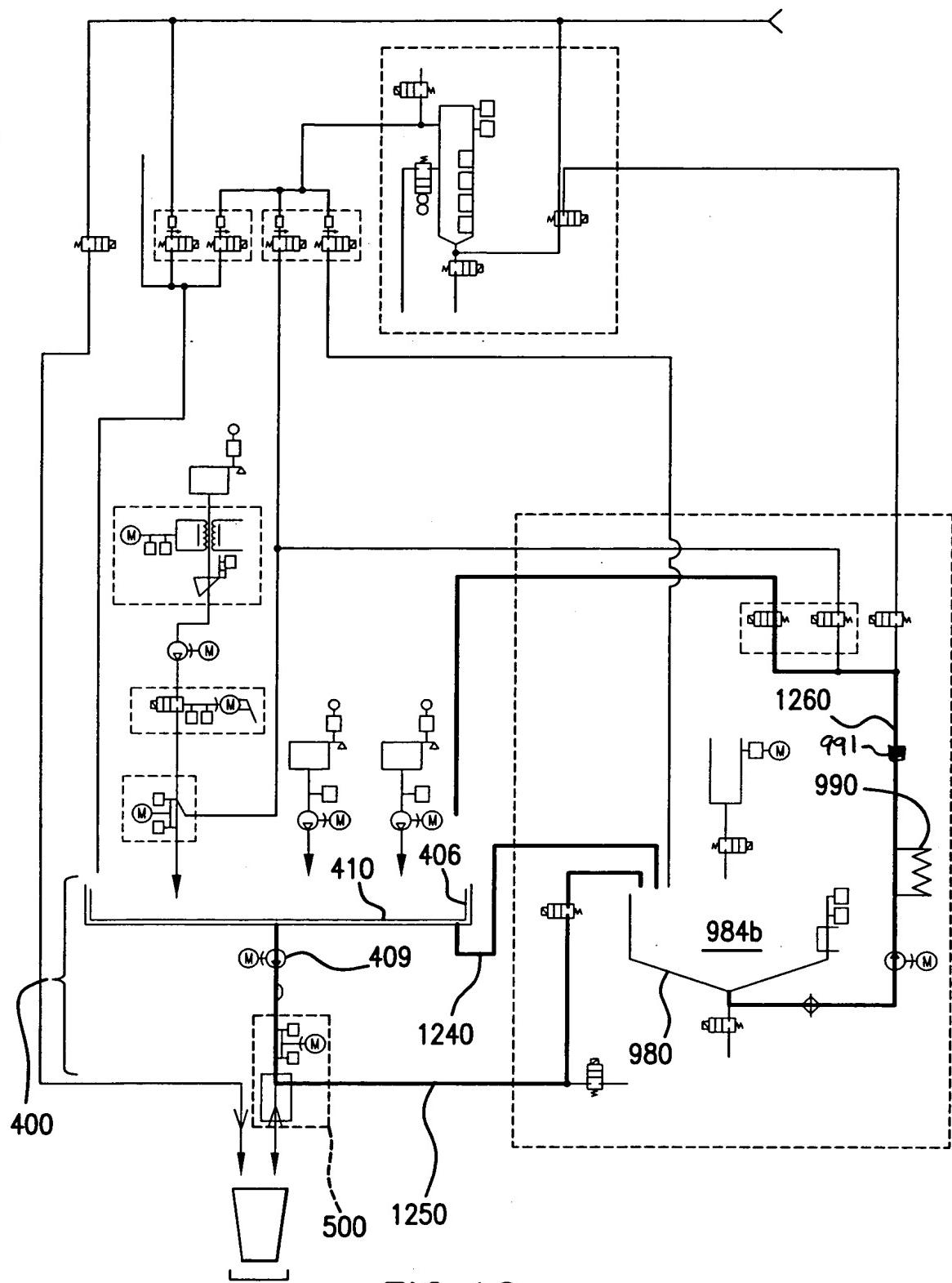


FIG.18

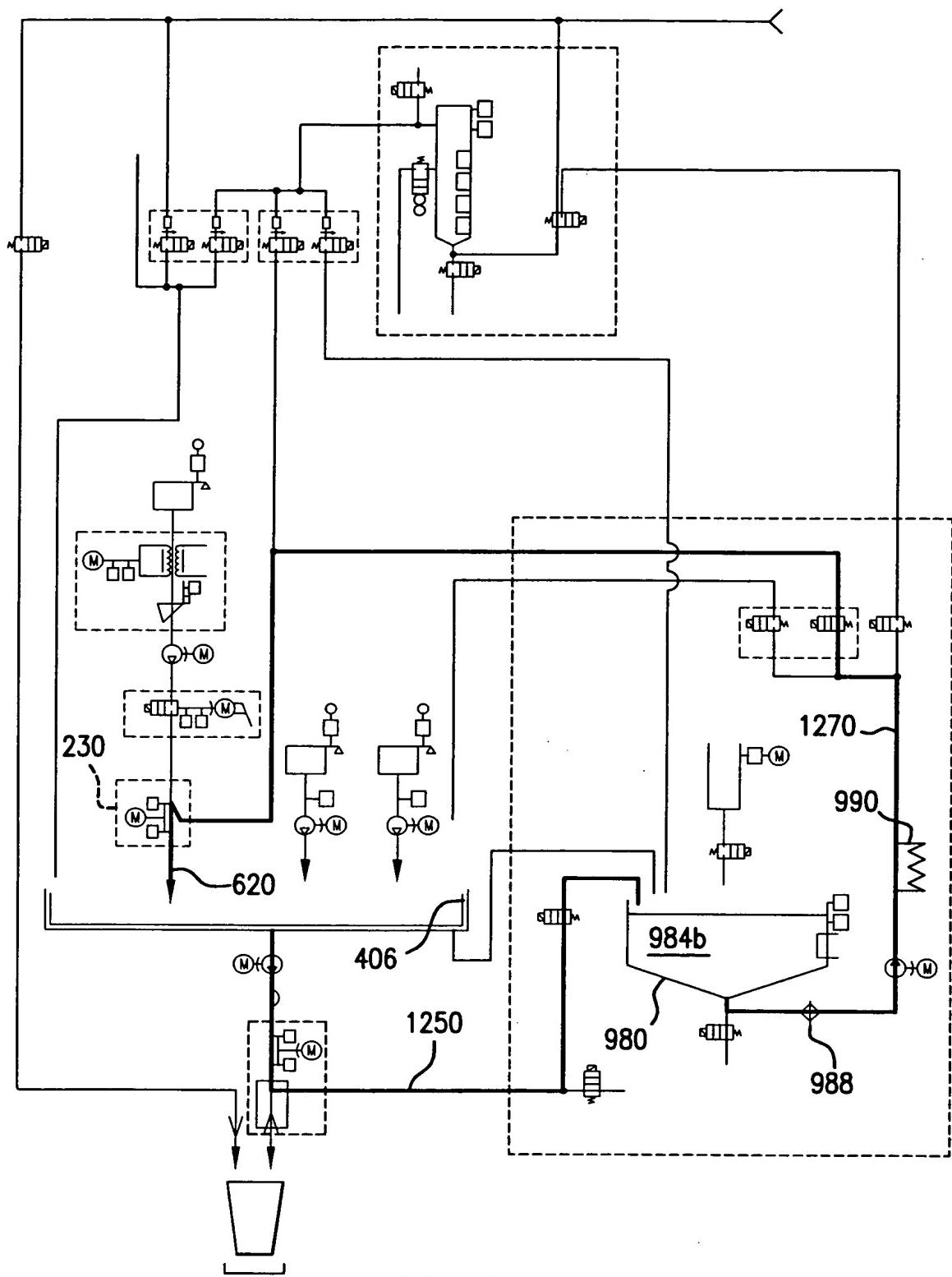


FIG.19

18120

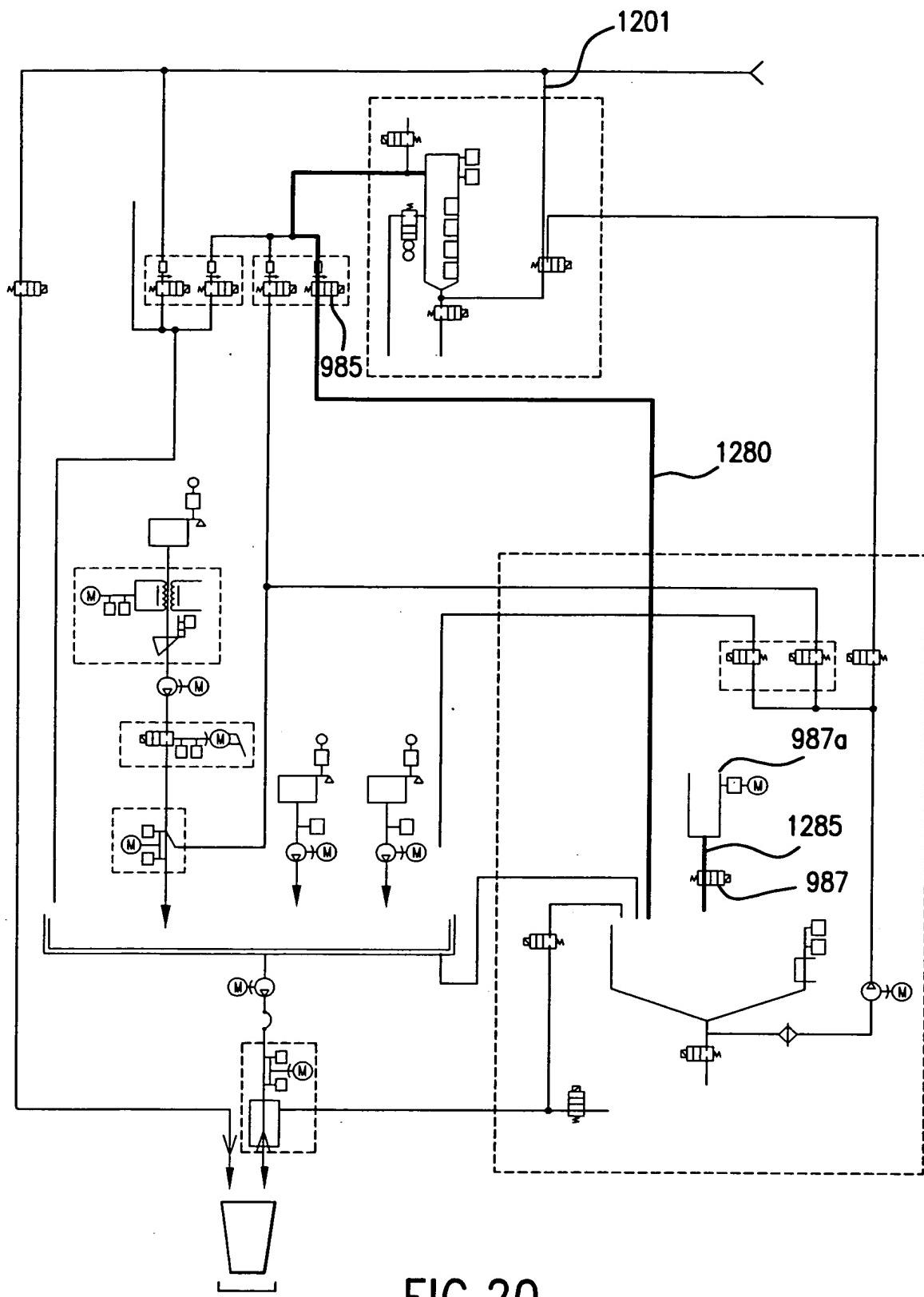


FIG.20

19120

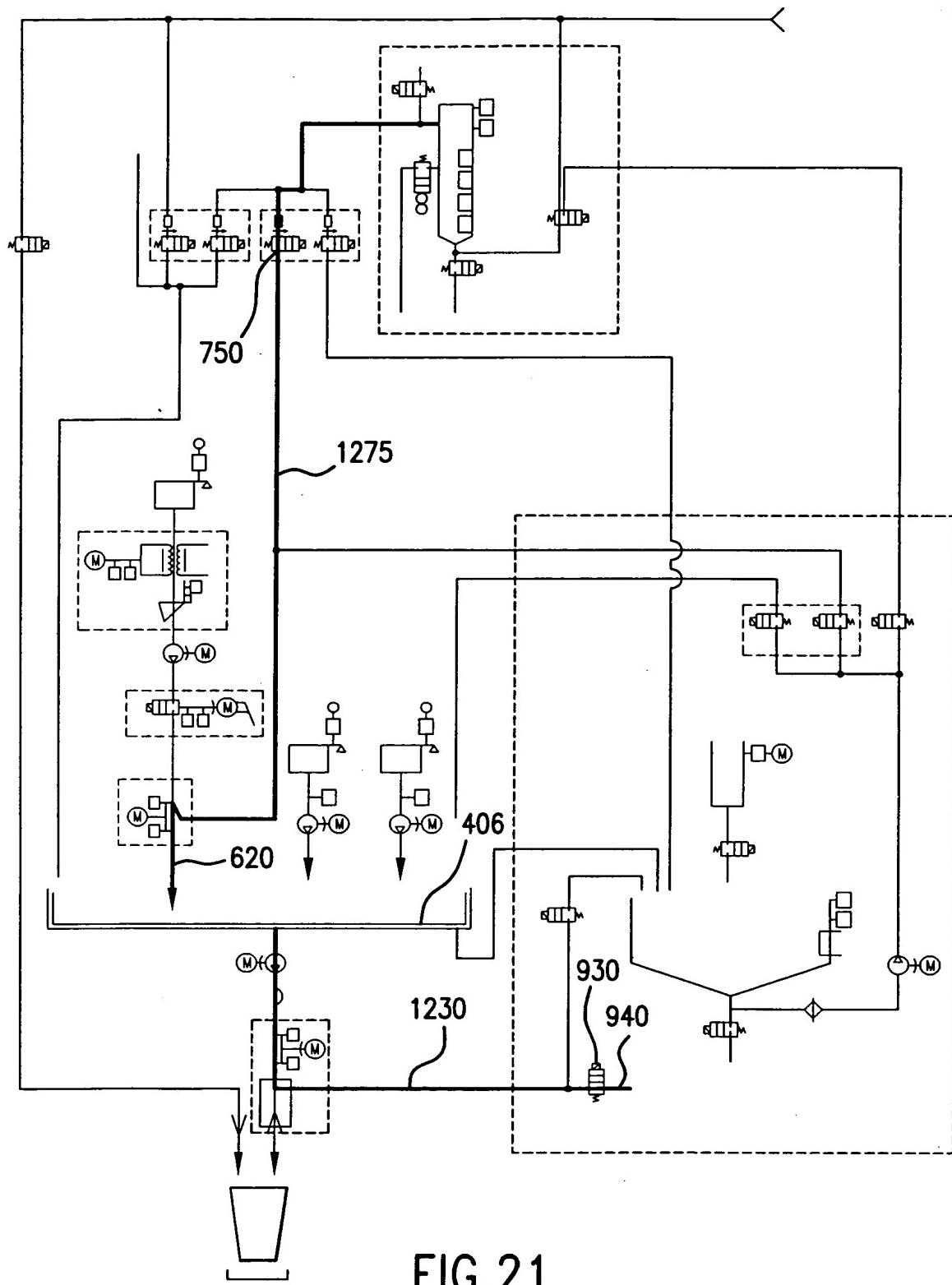


FIG.21

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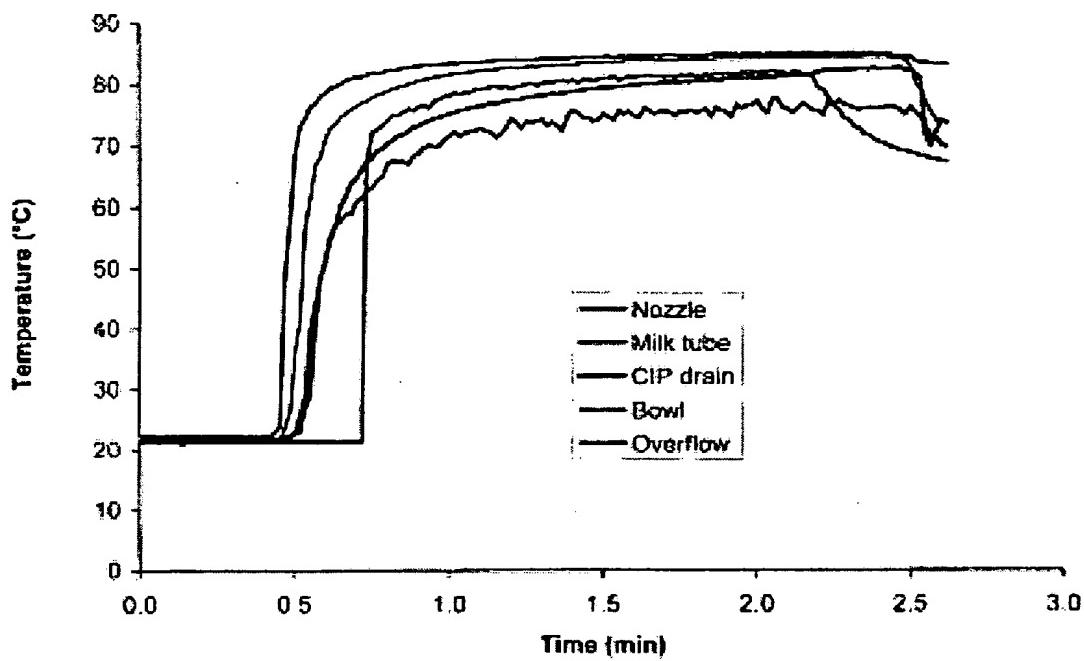


FIG. 22

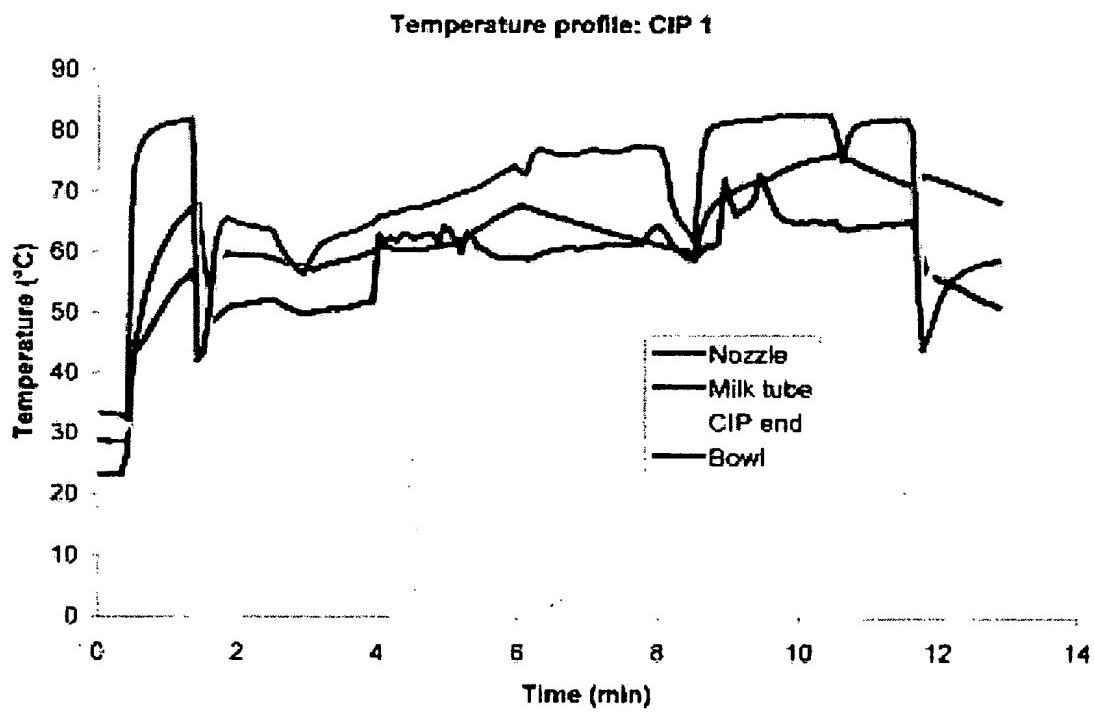


FIG. 23